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2. [Doxygen MailingList: Request for test criteria documentation su](#)   
... cfg diagrams/html/index.html: diagrams\_a.h ... test +GENERATE\_TAGFILE = test.tag +GENERATE\_LATEX  
= NO ... bool hideTestPlanFlag; // hide test plan documentation static ...  
[www.xeqt.com/doxygen/list-000714-000826/0052.html](http://www.xeqt.com/doxygen/list-000714-000826/0052.html) - 18k - [Cached](#)
3. [Plugins Test Plan](#)   
... goals as mentioned in section 1 of the test plan. ... com; Load  
[www.powershot.com/splash.html](http://www.powershot.com/splash.html).  
For Realplayer: ... via EMBED tag; via OBJECT tag; standard content; standard ...  
[www.mozilla.org/quality/browser/front-end/testplans/pluginstestplan.html](http://www.mozilla.org/quality/browser/front-end/testplans/pluginstestplan.html) - 36k - [Cached](#) - [More pages from this site](#)
4. [<!--title--> Wal-Mart Shelves RFID Test Plan <!--/title-->](#)   
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... (a new validation tag, the "diff-tag" has to ... He validates (optically) each > response (HTML-Page) according to the description in the test > plan ...  
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Latest STIS Test Plan on Paper (already changed, natch). ... N2-6 IM Pt Lamp Testing At a safe slit setting (compare with band 1 time-tag testing), take time-tag ...  
[hires.gsfc.nasa.gov/stis/stiscal/plans/plan\\_0413.html](http://hires.gsfc.nasa.gov/stis/stiscal/plans/plan_0413.html) - 29k - [Cached](#)
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8. [Macromedia - DevNet: Unit Testing ColdFusion Components with ...](#)   
... Your test case will automatically generate an HTML table of test results. ... can also be placed in a directory under a ColdFusion mapping, or in a custom tag root ...  
[www.macromedia.com/devnet/mx/coldfusion/articles/cfunit.html](http://www.macromedia.com/devnet/mx/coldfusion/articles/cfunit.html) - 31k - [Cached](#) - [More pages from this site](#)
9. [CVS test plan \(CE\)](#)   
... installing, look at this pages: <http://www.loria.fr/~molli/cvs-index.html> <http://download...>

one  
or more branches for the given file (Look at Add Tag command to ...  
[qa.netbeans.org/man-suites/guide\\_cvs.html](http://qa.netbeans.org/man-suites/guide_cvs.html) - 63k - Cached

10. **CUPS Configuration Management Plan**   
... Software testing shall be conducted according to the CUPS Software Test Plan, CUPS-  
STP ... [cmp.html,v](http://cmp.html,v)  
1.9 2003/05/27 17:40:43 mike Exp \$" tag: ... Id: cmp.html,v 1.9 ...  
[www.cups.org/cmp.html](http://www.cups.org/cmp.html) - 31k - Cached
11. **Testing Without a Formal Test Plan**   
... During your pedal-to-the-floor, no-test-plan testing project, boundary ... Everyone's  
favorite: enter in HTML code. ... name in BLINK tags, enter in an IMG tag for a ...  
[www.philosophe.com/testing/without\\_testplans.html](http://www.philosophe.com/testing/without_testplans.html) - 18k - Cached
12. **xml-dev - ID and tag value uniqueness, how?**   
... 1998-10-15</duedate> <whom>Joe Bob</whom> </assigned> <summary  
status="closed"> <resolution>Test  
plan modified and ... Follow-Ups: ID and tag value uniqueness, how ...  
[lists.xml.org/archives/xml-dev/199810/msg00513.html](http://lists.xml.org/archives/xml-dev/199810/msg00513.html) - 10k - Cached
13. **Standard Operating Procedures, Volume 2**   
... denoted as follows: **HTML Tag**, Denotation, Required Inspection. <Hn>,  
For ... of anchor target. **Inspection Test Plan** and Record. A functional ...  
[www.csc.calpoly.edu/~gfisher/classes/205/handouts/sop-testing.html](http://www.csc.calpoly.edu/~gfisher/classes/205/handouts/sop-testing.html) - 11k - Cached - More pages from this site
14. **Sample Year 2000 Test Plan**   
... **html**; GA Tech Application Test Plan Guidelines;  
[http://danshiki.oit.gatech.edu/html/documents/tag/App1\\_Test\\_Plan\\_Sect3.html](http://danshiki.oit.gatech.edu/html/documents/tag/App1_Test_Plan_Sect3.html);  
SAIC ...  
[www.state.ma.us/y2k/Archive/ProjPlanning/testplan.htm](http://www.state.ma.us/y2k/Archive/ProjPlanning/testplan.htm) - 33k - Cached - More pages from this site
15. **Wal-Mart shelves RFID test plan**   
... Wal-Mart shelves RFID test plan. ... These so-called smart shelves can read the RFID  
tag when an individual item is placed on the shelf and when it's removed. ...  
[www.nwfusion.com/news/2003/0714walmart.html](http://www.nwfusion.com/news/2003/0714walmart.html) - 53k - Cached
16. **Top 20 Web site analysis: Introduction**   
... Number of **HTML** elements includes all tags Number of bytes size of page; excludes  
images, style sheets, etc. Number of images specified by <IMG> tag Number of ...  
[www.veritest.com/bi/cont2000/200003/top20int.asp?visitor=X](http://www.veritest.com/bi/cont2000/200003/top20int.asp?visitor=X) - 69k - Cached - More pages from this site
17. **WebCGM Conformance Test Plan**   
... **OBJECT tag** in **HTML** tests (as opposed to non-standard EMBED)? [Preliminary: yes,  
but construct them minimally unless they are the point of the test.]; ...  
[www.cgmopen.org/technical/testing/task2-report-TOC.html](http://www.cgmopen.org/technical/testing/task2-report-TOC.html) - 95k - Cached
18. **OJI Test Plan**   
... Client QA Component Test Plan. ... Applet within <APPLET> tag should load fine.  
Functional  
and ... Tags; Lifecycle of an applet; Liveconnect functionality. **HTML** tags would ...  
[website-beta.mozilla.org/quality/browser/front-end/testplans/ojitestplan.html](http://website-beta.mozilla.org/quality/browser/front-end/testplans/ojitestplan.html) - 16k - Cached  
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19. **HTML 4 For Dummies - Bonus Chapter 4, Section 4**   
... pick up your Web page if you use the <!DOCTYPE> tag with a reference to HTML 4.0 until HTML 4.0 is ... These sketches are the first documents in your test plan. ...  
[www.lanw.com/html4dum/h4d4e/extras/ex04/e04s04.htm](http://www.lanw.com/html4dum/h4d4e/extras/ex04/e04s04.htm) - 25k - Cached

20. **TEST PLAN FOR PSD EM**   
... critical parameters of the PSD system since noise triggers can easily exceed the expected event rates, leading directly to an exceed in time-tag rates sent to ...  
[www.cesr.fr/~jurgen/psd/doc/um\\_psd\\_em\\_1.1/um\\_psd\\_em\\_1.1.html](http://www.cesr.fr/~jurgen/psd/doc/um_psd_em_1.1/um_psd_em_1.1.html) - 101k - Cached

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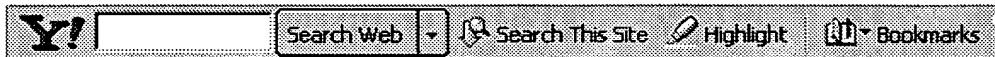
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... cfg diagrams/html/index.html: diagrams\_a.h ... test +GENERATE\_TAGFILE =

test.tag +GENERATE\_LATEX

= NO ... bool hideTestPlanFlag; // hide test plan documentation static ...

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For Realplayer: ... via EMBED tag; via OBJECT tag; standard content; standard ...

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## [Intelligent Send Feature Test Plan](#)

... Compose rich text using HTML; Option tag leaves as Auto Detect (default);

Select default (Ask me what to do) from Preference formatting; ...

[www.mozilla.org/quality/mailnews/tests/machv-mn-intelligentsend.html](http://www.mozilla.org/quality/mailnews/tests/machv-mn-intelligentsend.html) - 31k - Nov 22, 2003 - [Cached](#) - [Similar pages](#)

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... This document is an overall **test plan** that will serve as the ... Comment Tag Removal filter, Removes comment tags (<!-- cmnt -->) tags from an **HTML** page. ...  
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... Your test case will automatically generate an **HTML** table of test results. ... can also be placed in a directory under a ColdFusion mapping, or in a custom tag root ...  
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[Evolutionary Divide and Conquer for the Set-Covering Problem - Hernández \(1995\) \(Correct\) \(2 citations\)](#)

to try every single combination out. The **test plan** attempted to establish good parameter values by

[www.dai.ed.ac.uk/groups/evalg/Projects/MSc/1994\\_95/luisgh/outline.ps.gz](http://www.dai.ed.ac.uk/groups/evalg/Projects/MSc/1994_95/luisgh/outline.ps.gz)

[Unknown - Th Annual Naccq \(Correct\)](#)

7.1 Technical And Specification Testing The **Test Plan** For This Part Involves Checking The Integrity WBMP images (e.g 'Cross' image on the splash page)TagTag WAP Emulator ([www.tagtag.com](http://www.tagtag.com)) is an online image on the splash page)TagTag WAP Emulator ([www.tagtag.com](http://www.tagtag.com)) is an online emulator used to simulate the site.[tekotago.ac.nz/staticdata/papers02/papers/goh225.pdf](http://tekotago.ac.nz/staticdata/papers02/papers/goh225.pdf)

[Deliverable D6.1 Interim Report on the Results of the.. - Deliverable Type Pu \(Correct\)](#)

in each activity and the definition of a **test plan**. The activities in which substantial work has dedicated to a proprietary implementation by Cisco (tag switching)and has revealed that whilst the basic By The Directly Connected Routers. Mpls Architecture Tag Sw Tching On 900 Kbps Atm Pvpc (2200 Ce L /s) [www.dante.net/quantum/qtp/QUA-99-070.pdf](http://www.dante.net/quantum/qtp/QUA-99-070.pdf)

[Software Process Modeling using Functional Language Miranda - Matsuura, Honiden \(Correct\)](#)

a design, Modifying a design, Modifying a **test plan**. TK: Exchanging opinions with others, Deciding [www.ipa.go.jp/NEWSOFT/public/EVA/jcse.ps.gz](http://www.ipa.go.jp/NEWSOFT/public/EVA/jcse.ps.gz)

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Vers. 1.2 Pages 2426 4. Kin (P) GDT System **Test Plan** Version 2 Sections 3.7 3.12 5. Checklists (S) Members know the tools they should use Define tags for later use. Notes There is a Logical & & Priority Sequence. 1-5 Document Type Document Tag Document Name Version /Date Chunk or Sample [www.pimsl.com/TomGilb/AdvancedInspections.pdf](http://www.pimsl.com/TomGilb/AdvancedInspections.pdf)

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 Josep M. Ribó , Xavier Franch  
**Proceedings of the 14th international conference on Software engineering and knowledge engineering** July 2002  
We present the proactive behavioural features of PROMENADE, a process modelling language for formalizing the construction of software process models. PROMENADE aims at improving expressiveness, standardization, flexibility and reuse in software process modelling. In this article we focus on expressiveness, which is achieved by means of a declarative (instead of imperative) proactive control-flow based on precedence relationships. Different families of such precedences have been defined within th ...

**2 Multiprocessing design verification methodology for Motorola MPC74XX PowerPC microprocessor** 80%  
 Jen-Tien Yen , Qichao Richard Yin  
**Proceedings of the 37th conference on Design automation** June 2000  
Multiprocessing (MP) design verification has been one of the bottlenecks for high performance microprocessor design projects. The problem is getting worse as the design complexity increases and more cache structures are integrated into one single chip. The challenges that MP verification faces today include: huge chip/system simulation model sizes, long simulation cycles, relative inefficiency of the simulation tools compared to uniprocessor, and so on. To solve these challenging problems, ...

**3 Testing large software with automated software evaluation systems** 77%  
 C. V. Ramamoorthy , S. F. Ho  
**ACM SIGPLAN Notices , Proceedings of the international conference on Reliable software** April 1975  
Volume 10 Issue 6

In the past few years, research has been actively carried out in an attempt to improve the quality and reliability of large scale software systems. Although progress has been made on the formal proof of program correctness, proving large scale software systems correct by formal proof is still many years away. Automated software tools have been found to be valuable in improving software reliability and attacking the high cost of software systems. This paper attempts to describe some main fea ...

**4 Automated aids fob reliable software**

77%

 Donald J. Reifer

**ACM SIGPLAN Notices , Proceedings of the international conference on Reliable software** April 1975

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Recent investigations on the use of automation to realize the twin objectives of cost reduction and reliability improvement for computer programs developed for the U. S. Air Force are reported. The concepts of reliability and automation as they pertain to software are explained. Then, over twenty automated tools and techniques (aids) identified by this investigation are described and categorized. Based on the information reviewed, an assessment of the state of the technology is made. Finall ...

**5 An approach to document processing**

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 Robert Katz

**Proceedings of the 2nd annual international conference on Systems documentation** April 1983

**6 Effective software engineering pedagogy**

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 Evelyn Stiller , Cathie LeBlanc

**The Journal of Computing in Small Colleges** May 2002

Volume 17 Issue 6

Accreditation standards have required computer science departments to teach software engineering for over a decade now. Industry statistics, however, suggest that good software engineering techniques often are not being carried out in large-scale software development projects, resulting in unusable software systems on which millions of dollars have been squandered. Society cannot afford to continue to squander precious resources on software fiascoes. We suggest that perhaps how we teach software ...

**7 Pair programming and pair trading: effects on learning and motivation in a CS2 course**

77%

 Timothy H. DeClue

**The Journal of Computing in Small Colleges** May 2003

Volume 18 Issue 5

Pair programming is a seemingly powerful addition to the tool kit of educators wishing to enhance learning in software engineering students. This technique, found among professional software development practices associated with extreme programming (Beck), has probably existed informally for some time in various forms of collaborative software development. More recently pair programming has been formalized and used as an intentional pedagogical tool in the classroom. During the Spring semester o ...

**8 Book review: Review of: Back to the User: Creating User-Focused Web Sites**

77%

9 Abstraction-based software development 77%  
 Valdis Berzins , Michael Gray , David Naumann  
**Communications of the ACM** May 1986  
Volume 29 Issue 5  
A five-year experience with abstraction-based software-development techniques in the university environment indicates that the investment required to support the paradigm in practice is returned in terms of greater ability to control complexity in large projects&mdash;provided there exists a set of software tools sufficient to support the approach.

10 Teaching future verification engineers: the forgotten side of logic design 77%  
 Fusun Ozguner , Duane Marhefka , Joanne DeGroat , Bruce Wile , Jennifer Stofer , Lyle Hanrahan  
**Proceedings of the 38th conference on Design automation** June 2001  
This paper describes a senior/graduate level course in hardware logic verification being offered by The Ohio State University in cooperation with IBM. The need for the course is established through the growing importance of logic verification to users of custom logic designs. We discuss the short-term and long-term goals for the course, and describe the course content and format. The course relies heavily on lab projects to illustrate the main concepts. Three projects and a final project re ...

11 Information systems curriculum recommendations for the 80s: undergraduate and graduate programs 77%  
 Jay F. Nunamaker , J. Daniel Couger , Gordon B. Davis  
**Communications of the ACM** November 1982  
Volume 25 Issue 11  
The recommendations of the 1972 and 1973 ACM Curriculum Committee on Information Systems programs have been influential in the development of degree programs at the bachelor's, master's, and doctoral levels. The earlier curriculum has been revised and updated based on advances in the field over the past nine years. The report discusses the continuing need for education related to the definition, analysis, design, construction, and management of information systems in organizations. The stru ...

12 Mining features for sequence classification 77%  
 Neal Lesh , Mohammed J. Zaki , Mitsunori Ogihara  
**Proceedings of the fifth ACM SIGKDD international conference on Knowledge discovery and data mining** August 1999

13 An object-oriented approach to automated generation of challenge examinations using Ada 95 77%  
 Arthur Irving Littlefield  
**ACM SIGAda Ada Letters** January 1997  
Volume XVII Issue 1  
The primary objective of this paper is to analyze and evaluate the usefulness of object-oriented development and the Ada 95 programming language as applied to a specific software development project. A secondary objective is to show that structured development is still useful

while applying object-oriented development and that the two methods can be integrated. The project is to develop an automated tool for generation of challenge examinations to test the knowledge of students in a given subjec ...

**14 Developing single-source documentation for multiple formats**

77%

 Cindy Roposh , Hanna Schoenrock

**Proceedings of the 14th annual international conference on Systems documentation:  
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**15 A real world conversion to SGML**

77%

 Dee Stribling , Tim Hunter , Len Olszewski , Anne Corrigan , Randy Mullis , Lloyd Allen

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Weininger, S.;

Bioengineering Conference, 2003 IEEE 29th Annual, Proceedings of , 22-23 March 2003

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**2 Test and evaluation experience with an untethered supervisory controlled undersea robot**

Walton, J.M.;

OCEANS '96. MTS/IEEE. 'Prospects for the 21st Century'. Conference Proceedings , Volume: 2 , 23-26 Sept. 1996

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Hopkins, M.;

Electromagnetic Compatibility Proceedings, 1997 International Symposium on , 21-23 May 1997

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**4 Rotorcraft flight test automation-the saga continues**

*Carico, D.;*

Aerospace Conference, 1998. Proceedings., IEEE , Volume: 3 , 21-28

March 1998

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**5 Automated generation of SPICE characterization test masks and test databases**

*Kasel, L.; McAndrew, C.C.; Drennan, P.; Davis, W.F.; Ida, R.;*

Microelectronic Test Structures, 1999. ICMTS 1999. Proceedings of the 1999 International Conference on , 15-18 March 1999

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